FIG. 1 (PRIOR ART)

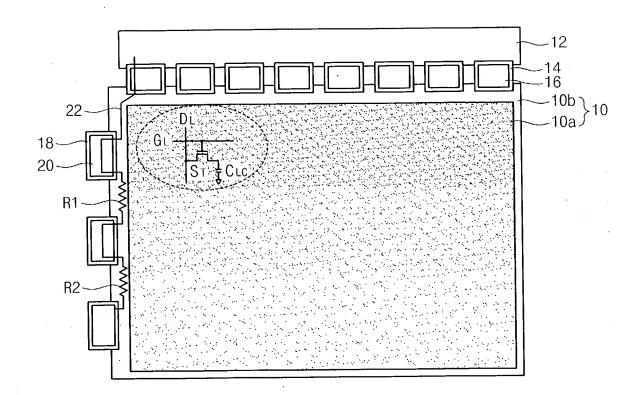


FIG.2
(PRIOR ART)

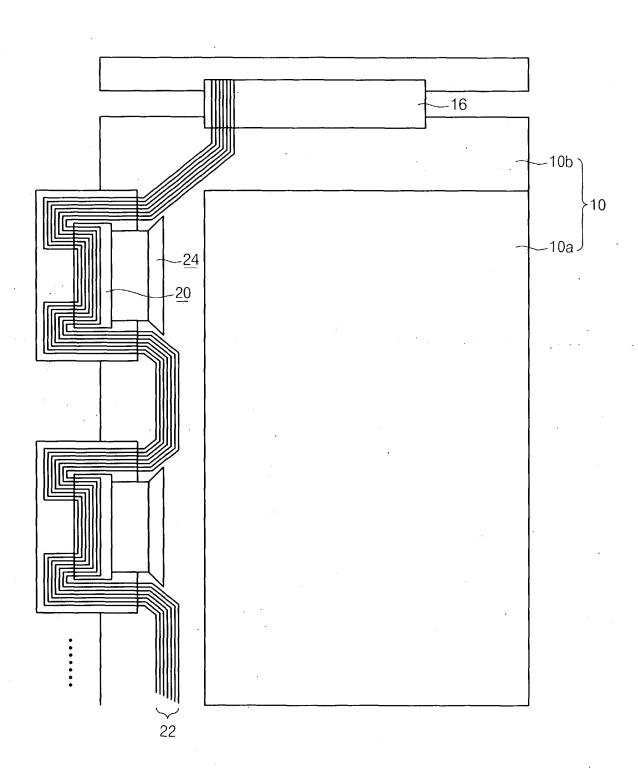


FIG.3

(PRIOR ART)

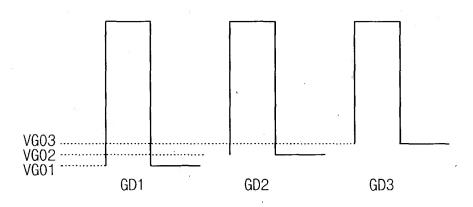


FIG.4

(PRIOR ART)

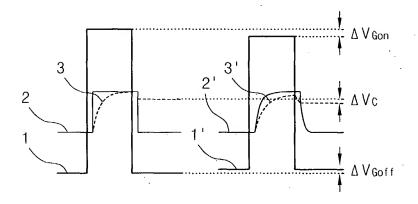


FIG.5

(PRIOR ART)

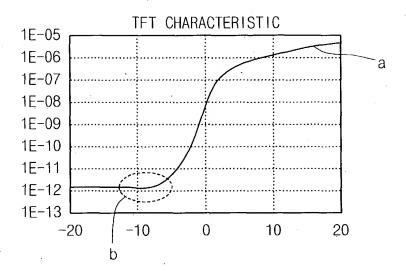


FIG.6

(PRIOR ART)

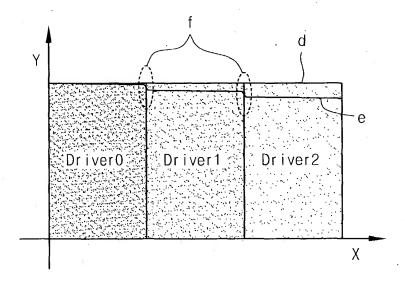


FIG.7

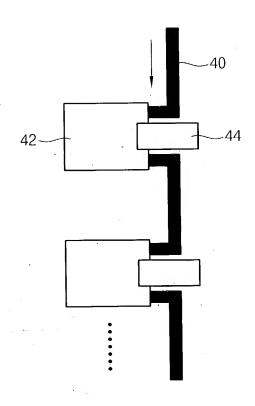


FIG.8

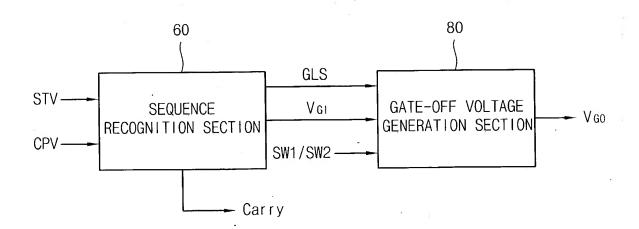


FIG.9

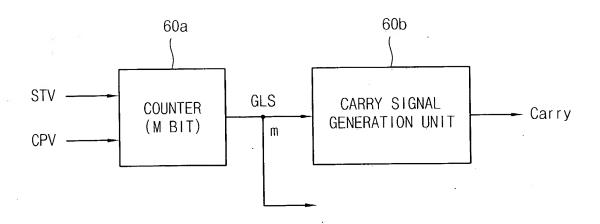


FIG.10

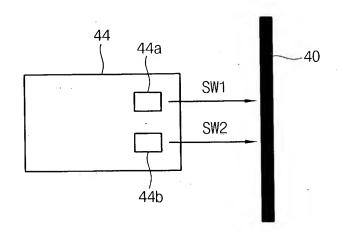


FIG.11

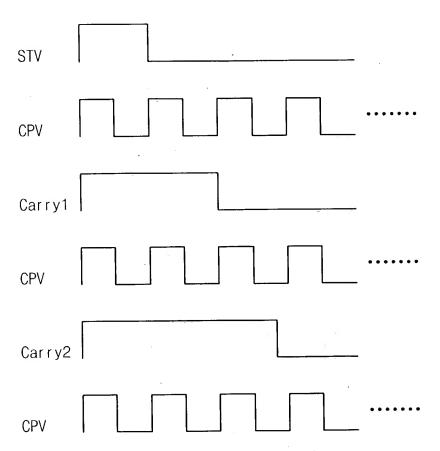
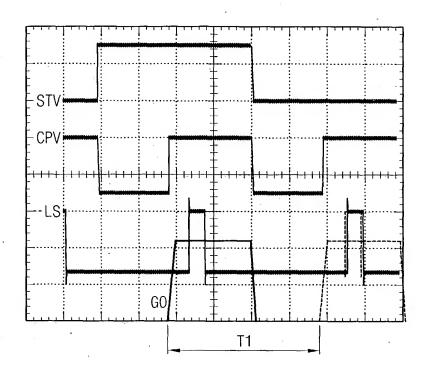


FIG.12



Support Control

FIG.13

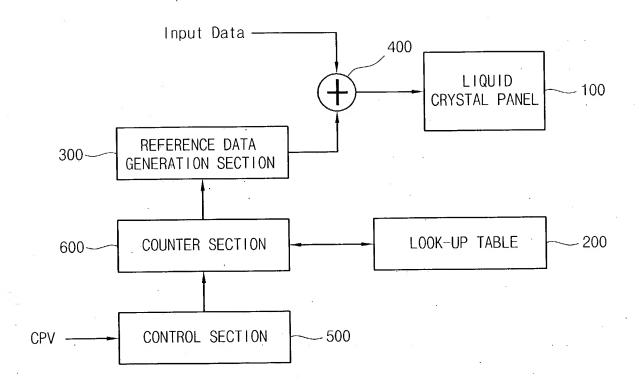


FIG.14

GDN	REF
0	REF 0
1	REF 1
2	REF 2
3	REF 3
. n−1 ·	REF n−1

FIG.15

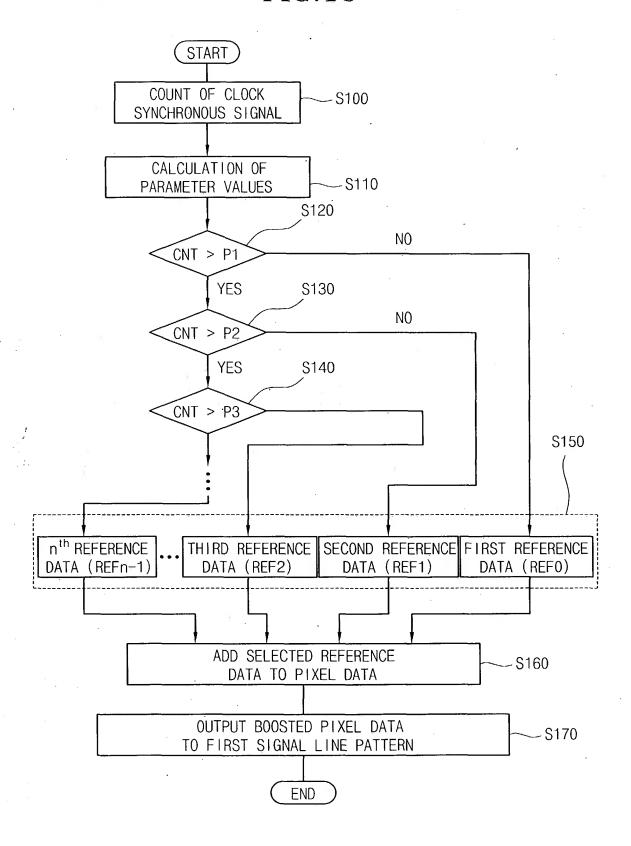


FIG.16

